

IoT Hardware Security Analysis

Security aspects of the device hardware.

- Review of hardware layout and schematics regarding secure design
- Verification of debugging interface security, e.g.
 - JTAG, SWD
 - USB, UART, SPI
 - CAN, I²C
- Flash rom and memory analysis
- Glitching tests
- Devices are analysed in our main security lab in Germany

Price from
2.500€

